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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	10/812,395
				Filing Date	March 30, 2004
				First Named Inventor	Egerer
				Art Unit	2836
				Examiner Name	Rutland Wallis, Michael
Sheet	1	of	1	Attorney Docket Number	54382-20048.00

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
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FOREIGN PATENT DOCUMENTS						
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		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
/M.R./	1	DE 19648492 A1	11-13-97	Siemens AG		
/M.R./	2	DE 3806951 C2	09-15-88	SGS-Thomson Microelectronics S.P.A.		

Examiner Signature	/Michael Rutland Wallis/ (06/22/2007)	Date Considered	06/22/2007
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